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APPLICATION
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For: **PLASMA DISPLAY PANEL HAVING**
A SEALING STRUCTURE FOR
REDUCING NOISE
Docket No.: **6161.0099.AA**

PLASMA DISPLAY PANEL HAVING SEALING STRUCTURE FOR REDUCING NOISE

CROSS REFERENCE TO RELATED APPLICATION

5 This application is based on Korean Patent Application No. 2002-0073949 filed on November 26, 2002 in the Korean Intellectual Property Office, the content of which is incorporated herein by reference in its entirety.

BACKGROUND OF THE INVENTION

(a) Field of the Invention

10 The present invention relates to a plasma display panel, and more particularly, to a structure for joining substrates of a plasma display panel.

(b) Description of the Related Art

Flat panel displays are used for wall-mounted televisions, computer screens, and other such display applications. Among the different types of flat panel displays, the plasma display panel (PDP) is emerging as one of the most promising flat panel display configurations. Predetermined images are realized by the PDP by a discharge mechanism occurring in discharge cells.

As with other flat panel displays, such as, vacuum fluorescent displays and field emission displays, PDPs include two substrates (hereinafter referred to as an upper substrate and a lower substrate) which are provided substantially in parallel with each other and with a predetermined gap therebetween. The substrates define an exterior of the display device. A sealant is provided around an outer circumference of opposing surfaces of the substrates to join the substrates together. Air is evacuated from between the substrates in order to obtain a vacuum assembly.

The sealant is typically made of a sealant glass, or frit. During manufacture of the PDP, the sealing process is performed by subjecting the substrates with the frit therebetween in an environment with a temperature that is higher than a temperature corresponding to a softening point of the frit to thereby seal the substrates. A predetermined pressure (e.g., 1~2kg/cm²) may 5 be applied to an exterior of the substrates to realize more effective sealing. Such a pressure may be applied, for example, by a plurality of sealant clips that apply pressure to the substrates.

As an example of a technique for sealing a PDP, a sealing method for a PDP is disclosed in Korean Laid-Open Patent No. 2001-0004156. However, as disclosed in the patent, in the sealing process of flat panel displays, including PDPs, there is a high probability that minute 10 leaks will occur at portions of the sealant area because of the joining characteristics of the frit and the upper and lower substrates.

Such a problem may be attributed to the state of deposition of the frit on the substrates. That is, the frit is generally deposited, with a uniform thickness, around the circumference of the substrates. No steps are taken to vary the thickness of the frit at specific areas, such as, the areas 15 where the sealant clips are provided. As a result, the thickness of the frit varies in the regions where the sealant clips are mounted on the substrates.

In particular, the frit in the region where the sealant clips are provided becomes thinner than the frit where the sealant clips are not provided (a difference of approximately 20~40μm results). If minute gaps are formed, as a result of this difference in frit thickness in the regions 20 where the substrates are sealed, noise is generated during operation of the PDP. This reduces the overall quality of the PDP.

SUMMARY OF THE INVENTION

In one embodiment, the invention provides a plasma display panel that substantially prevents the formation of minute gaps in a sealing area between substrates to thereby reduce noise caused by such minute gaps.

5 The plasma display panel includes a first substrate and a second substrate opposing one another and with a predetermined gap therebetween. A sealant is formed on opposing surfaces of the first substrate and the second substrate around outer circumferential areas of the first substrate and the second substrate to seal the first substrate and the second substrate. The sealant is formed of regions having a first width of substantially the same size and of regions having a 10 second width greater than the size of the first width.

In various embodiments according to this invention, the plasma display panel includes a first substrate and a second substrate which oppose one another with a predetermined gap therebetween, and a sealant which is formed on opposing surfaces of the first substrate and the 15 second substrate around outer circumferential areas of the first substrate and the second substrate to seal the first substrate and the second substrate. The cross-section of sealant is band-shaped with a plurality of nodes.

The invention separately provides a method for sealing a first substrate of a plasma display panel with a second substrate of the plasma display panel, the method comprising 20 depositing a sealant along an outside border of the first substrate, wherein the sealant is deposited on a surface of the first substrate which opposes the second substrate and the sealant has a first width, which is substantially uniform, in a plurality of first areas and the sealant has a second width in second areas.

BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings, which are incorporated in and constitute a part of the specification, illustrate an exemplary embodiment of the invention, and, together with the 5 description, serve to explain the principles of the invention.

FIG. 1 is a partial cutaway perspective view of a plasma display panel according to an embodiment of the present invention.

FIG. 2 is a plan view of a plasma display panel according to an embodiment of the present invention.

10 FIG. 3 is a front view of a plasma display panel according to an embodiment of the present invention.

FIGS. 4, 5, and 6 are schematic views used to describe a sealing process of a plasma display panel according to an embodiment of the present invention.

15 DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

An exemplary embodiment of the present invention will now be described in detail with reference to the accompanying drawings. It should be understood that the structure of the present invention is useful not only for plasma display panels, but also for similar flat panel displays, such as vacuum fluorescent displays.

20 FIG. 1 is a partial cutaway perspective view of a plasma display panel according to an embodiment of the invention. As shown in the drawing, the plasma display panel (PDP) includes a first substrate (or upper substrate) 20 and a second substrate (or lower substrate) 22 provided substantially parallel with each other and with a predetermined gap therebetween. Also, various

structural elements are provided between the first substrate 20 and the second substrate 22 for realizing the display of predetermined images according to operation of a discharge mechanism. More particularly, for example, mounted between the first substrate 20 and the second substrate 22 are barrier ribs for forming discharge cells, discharge sustain electrodes and address electrodes to which voltages needed for discharge are applied, phosphor layers, and a dielectric layer.

Generally, the first substrate 20 and the second substrate 22 are substantially rectangular, and thus have long sides and short sides. A sealant 24 is deposited on outer circumference areas of at least one of the first substrate 20 and the second substrate 22. In particular, the sealant 24 is deposited on the outer circumference of at least one of the first substrate and the second substrate at portions of the substrate which oppose a surface of the other substrate. The first substrate 20 and the second substrate 22 are then attached to one another through a sealing process to thereby form the exterior of the PDP.

With reference also to FIG. 2, the sealant 24 is deposited in a non-display region 26 of the panel and in a substantially rectangular shape. Generally, the sealant 24 is deposited in a shape which corresponds to the configuration of the first substrate 20 and the second substrate 22. The sealant 24 is typically realized using frit, which is fused glass. In the present invention, following the sealing process of the PDP, a final form of the sealant 24 is realized, as described below, to prevent minute gaps from forming between the first substrate 20 and the second substrate 22.

Referring to FIG. 3, the sealant 24 has a predetermined thickness (t) between the first substrate 20 and the second substrate 22. However, when viewed from above, as in FIG. 2, there are areas of the sealant 24 having a width w2 that is greater than a width w1 of other areas of the

sealant 24. That is, the sealant 24 is formed having the width w1, and a plurality of nodes 24a are formed at predetermined areas of the sealant 24. At the nodes 24a, the sealant has a width w2, which is greater than the width w1.

The nodes 24a, having the width w2, gradually increase in size have a peak width w2, 5 and then gradually decrease in size until they have a width w1. However, the present invention is not limited to such a configuration and other various shapes may be used.

In the various embodiments of this invention, the nodes 24a having the width w2 are located at areas which correspond to areas where pressure is applied to the first substrate 20 and the second substrate 22 during the sealing operation. That is, the nodes 24a preferably 10 correspond to areas where the sealant clips are mounted on the first substrate 20 and the second substrate 22.

The sealing of the first substrate and the second substrate 22 will now be described with reference to FIGS. 4, 5, and 6.

First, with reference to FIG. 4, the sealant 24 is deposited on the outer circumferential 15 area of at least one of the first substrate 20 and the second substrate 22 on which the various structural elements are formed for displaying images (i.e., the discharge sustain electrodes, address electrodes, phosphor layers, and dielectric layer). The second substrate 22 is arbitrarily chosen to illustrate the process. The sealant 24 is deposited, for example, by a general adhesive deposition method using a dispenser 30 or by a screen printing method.

20 During deposition of the sealant 24 on predetermined areas of the substrate, the sealant 24 is deposited with a greater width than the remaining areas of the sealant 24. By depositing the sealant 24 with a greater width in some areas, the nodes 24a are formed. Such control of widths

is realized, for example, by varying an injection speed of the dispenser 30 and by controlling paste injection amount of the frit.

After depositing the sealant 24 on the second substrate 22, as described above, the first substrate 20 is placed on top of the second substrate 22, as shown in FIG. 5. The first substrate 5 20 and the second substrate 22 are then placed in an oven that is set at a temperature at or greater than the softening point of the sealant 24. By subjecting the first substrate 20 and the second substrate 22 to a temperature equal to or more than the softening point of the sealant, the first substrate 20 and the second substrate 22 may be sealed together. During this procedure, sealant clips 32 are mounted on the first substrate 20 and the second substrates 22 at areas corresponding 10 to the positions of the nodes 24a. The sealant clips 32 improve the seal between the first substrate 20 and the second substrate 22.

If the first substrate 20 and the second substrate 22 are sealed through such a process, it can be expected that a thickness of the sealant 24 corresponding to where the sealant clips 32 are located (i.e., where the sealant clips 32 are applying pressure to the first substrate 20 and the 15 second substrate 22) will be somewhat less than the thickness of the sealant 24 in other areas. However, in this invention, because these areas of the sealant 24 are formed with a greater width than the remaining areas of the sealant 24, the thickness at these areas (that is, at the nodes 24a) remains substantially the same as the other areas of the sealant 24. The result is that the thickness at substantially all areas of the sealant 24 is substantially uniform following the sealing 20 operation.

Further, as a result of the substantially uniform thickness of the sealant 24, minute gaps are not formed between the first substrate 20 and the second substrate 22. Table 1 below shows the results of noise measurements taken with this invention and with the conventional PDP of the

same basic type (in the conventional PDP, the sealant is deposited at a uniform width throughout its entire length). It is clear from the results of Table 1 that the PDP of this invention generates significantly less noise than the conventional PDP.

[Table 1]

Frequency bandwidth	Present Invention (dB)	Prior Art (dB)
2.0kHz bandwidth	9.7	15
2.5kHz bandwidth	13.4	20
3.15kHz bandwidth	13.9	17.6
Entire audible sound bandwidth (50Hz~8kHz)	22	27.3

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It is to be noted that the sealant 24 of this invention exhibited variations in thickness of about 5 μ m or less at different areas , while the sealant of the conventional PDP exhibited variations in thickness of about 20 μ m and 40 μ m .

In the panel displays according to this invention, as described above, the formation of 10 minute gaps between the substrates is prevented by an improved sealing structure. Therefore, noise generated during operation of the panel as a result of such minute gaps is reduced and an improved panel is provided.

Although an exemplary embodiment of the present invention has been described in detail hereinabove, it should be clearly understood that many variations and/or modifications of the 15 basic inventive concepts herein taught which may appear to those skilled in the present art will still fall within the spirit and scope of the present invention, as defined in the appended claims.